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	Complete if Known		
Application Number	TBA		
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First Named Inventor	Hiroyuki Nitta		
Group Art Unit	TBA		
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OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS						
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²			
JN		Miyashita et al., "A Novel Bit-Line Process Using Poly-Si Masked Dual-Damascene (PMDD) for 0.13 μm DRAMs and Beyond", IEDH Tech. 2000, pp. 361-362, December,2000				

Examiner Signature	JOSEPH MAUYEN	Date Considered	7/26/04	

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